

15920 U.S. PTO  
040204

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17497 U.S. PTO  
10/816727

040204

Atty. Docket No.: M-15372 US

April 2, 2004

Mail Stop Patent Application  
Commissioner For Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Inventor(s): Kook-Hwan Kwon and Steve S. Eaton  
Title: Multistage Parallel-To-Serial Conversion of Read Data In Memories, With The First Serial Bit Skipping At Least One Stage  
X Return Receipt Postcard  
X This Transmittal Letter (in duplicate)  
17 page(s) Specification (not including claims)  
4 page(s) Claims  
1 page Abstract  
14 Sheet(s) of Drawings  
2 page(s) Declaration For Patent Application and Power of Attorney  
1 page Recordation Form Cover Sheet (in duplicate)  
2 page Assignment  
1 Page Information Disclosure Statement under 37 CFR 1.97(b)  
1 Page Form PTO-1449 citing 16 references  
X Copies of 7 Cited References

CLAIMS AS FILED

For	Number Filed		Number Extra		Rate		Basic Fee
Total Claims	21	-20 =	1	X	\$18.00 =	\$	770.00 18.00
Independent Claims	3	-3 =	0	X	\$86.00 =	\$	0.00
<input type="checkbox"/> Fee of _____ for the first filing of one or more multiple dependent claims per application							\$

Please make the following charges to Deposit Account No. 50-2257:

- ☒ Total fee for filing the patent application in the amount of \$ 788.00  
☒ The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account No. 50-2257.

EXPRESS MAIL LABEL NO:

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Respectfully submitted,

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